L Number	Hits	Search Text	DB	Time stamp
1	2353	((integrated adj circuit) ic asic) near4 (PLD\$1 PAL\$1 FPLA\$1 CPLD\$1 EPLD\$1 EEPLD\$1 LCA\$1 FPGA\$1)	USPAT	2004/02/12 14:44
3	28	(((integrated adj circuit) ic asic) near4 (PLD\$1 PAL\$1 FPLA\$1 CPLD\$1 EPLD\$1 LCA\$1 FPGA\$1)) same wireless	USPAT	2004/02/12 14:35
4	136		USPAT	2004/02/12 17:01
5	6		USPAT	2004/02/12 14:48
6	1		USPAT	2004/02/12 14:59
7	21		USPAT	2004/02/12 15:13
8	0	,,	USPAT	2004/02/12 15:13
9	44	processor near2 pld	USPAT	2004/02/12 16:23
10	8	(processor near2 pld) same ic	USPAT	2004/02/12
11	640	wireless near6 ((integrated adj circuit)	USPAT	2004/02/12
12	18	((integrated adj circuit) ic asic) near4 (wireless adj transceiver)	USPAT	2004/02/12
13	8	("3384832" "3836706" "5623231" "5734298" "5963094" "6005438" "6046641" "6094084").PN.	USPAT	2004/02/12 16:50
14	122	,	USPAT	2004/02/12 16:51
15	23	xilinx and (wireless adj transceiver)	USPAT	2004/02/12
16	7	xilinx and (wireless adj transceiver) and	USPAT	2004/02/12
17	1067	1	USPAT	2004/02/12 17:01
18	7	(((integrated adj circuit) IC) near4 transmitter) same (PLD\$1 PAL\$1 FPLA\$1 CPLD\$1 EPLD\$1 EEPLD\$1 LCA\$1 FPGA\$1)	USPAT	2004/02/12 17:02
-	17633	PDC\$1 or PDL\$1 or "PDC" or "PDCs" or "PDL" or "PDLs" or (program\$5 adj logic adj (circuit or device))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/02/12 14:31
_	17633	PDC\$1 or PDL\$1 or "PDC" or "PDCs" or "PDL" or "PDLs" or (program\$5 adj logic adj (circuit or device))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/11/14 16:55
_	930585	eras\$3 or program\$5 or reprogram\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/11/14
_	9309	(PDC\$1 or PDL\$1 or "PDC" or "PDCs" or "PDL" or "PDLs" or (program\$5 adj logic adj (circuit or device))) same (eras\$3 or program\$5 or reprogram\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/08 15:54

-	0	(processor or microprocessor) near4 pld	USPAT;	2003/09/11
		near8 (die encas\$3)	US-PGPUB;	14:59
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	9	(processor microprocessor) same pld same	USPAT;	2003/09/11
		(die encas\$3)	US-PGPUB;	15:00
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	2	6389321.pn.	USPAT;	2004/02/10
			US-PGPUB;	16:21
			EPO; JPO;	
			DERWENT;	
	_		IBM_TDB	0004/00/00
-	2	5635855.pn.	USPAT;	2004/02/10
			US-PGPUB;	16:22
			EPO; JPO;	
			DERWENT;	ĺ
			IBM_TDB	
-	254966	(integrated adj circuit) IC	USPAT	2004/02/10
		1,,,,,		16:40
-	1330	((integrated adj circuit) IC) near4	USPAT	2004/02/10
		processor near4 memory		16:45
-	3	(((integrated adj circuit) IC) near4	USPAT	2004/02/10
		processor near4 memory) same pld		16:41
-	189	((integrated adj circuit) IC) near2	USPAT	2004/02/10
		(includ\$3 compris\$3 ha\$4) near2 processor		16:49
		near4 memory		
_	61	13.clm.	USPAT	2004/02/10
				16:48
-	8	13.ab.	USPAT	2004/02/10
				16:48
-	42		USPAT	2004/02/11
		(includ\$3 compris\$3 ha\$4) near2 processor		16:28
		near4 memory) same \$2program\$4		
_	6	("4656592" "5752070" "5898677"	USPAT	2004/02/11
		"6152613" "6269277" "6269467").PN.		16:09
-	78	processor near4 memory near4 (pld plc pla	USPAT	2004/02/11
		pga)		16:34
-	331	716/16.ccls.	USPAT	2004/02/11
				16:35
-	4624	716/\$.ccls.	USPAT	2004/02/11
				16:35
-	356	716/17.ccls.	USPAT	2004/02/11
				16:40
-	2313	\$2program\$4 near4 (pld (programmable adj	USPAT	2004/02/11
		logic adj device) plc)		16:43
-	146	(\$2program\$4 near4 (pld (programmable ad)	USPAT	2004/02/11
		logic adj device) plc)) and 716/\$.ccls.		16:43
-	54	(\$2program\$4 near4 (pld (programmable adj	USPAT	2004/02/11
		logic adj device) plc)) and 716/17.ccls.		16:45
_	46	1 1 1 2 1	USPAT	2004/02/11
		adj logic adj device) plc)) and		17:00
		716/17.ccls.) and (integrate\$1 near2		
		circuit)		
-	9185	(integrate\$1 near2 circuit) same	USPAT	2004/02/11
		processor same memory		16:52
_	1	((P - 9 -	USPAT	2004/02/11
		adj logic adj device) plc)) and		16:53
		716/17.ccls.) and ((integrate\$1 near2		
		circuit) same processor same memory)		
-	1580	(single near2 circuit) same processor	USPAT	2004/02/11
		same memory		16:53
-	1	6421812.pn.	USPAT	2004/02/11
		1		17:00
-	120		USPAT	2004/02/11
		adj logic adj device) plc)) and		17:17
		716/\$.ccls.) and (integrate\$1 near2		
		circuit)		